
QSFP+ Active Copper Cable

Description

The QSFP+ cable assemblies are high performance, cost effective I/O solutions for LAN, HPC and SAN. High speed cable assemblies meet and exceed Gigabit Ethernet, InfiniBand and Fiber Channel commercial temperature requirements for performance and reliability. The cables are compliant with InfiniBand Architecture, SFF-8436 specifications and provide connectivity between devices using QSFP ports.

Features (Low Power Version)

- QSFP+ conforms to the Small Form Factor SFF8436
 - 4-Channel Full-Duplex Active Copper Cable Transceiver
 - Support for multi-gigabit data rates :1GGb/s - 10Gb/s (per channel)
 - Maximum aggregate data rate: 40Gb/s (4 x 10Gb/s)
 - Copper link length up to 12m (active limiting)
 - High-Density QSFP 38-PIN Connector
 - Power Supply :+3.3V
 - Power Consumption: <0.8W
 - Low crosstalk
 - I2C based two-wire serial interface for EEPROM signature which can be customized
 - Temperature Range: 0~ 70 °C
 - ROHS Compatible
-

Applications

- 10 Gigabit Ethernet
 - 40 Gigabit Ethernet
 - InfiniBand 4x SDR, DDR, QDR
 - 2, 4, 8, 10 Gigabit Ethernet
 - Fiber Channel over Ethernet
 - SAS, Servers, Hubs, Switches, Routers
-

Standards Compliance

- √ IEEE 802.3ba
- √ SFF-8436
- √ InfiniBand
- √ QSFP+ MSA Page 2 of 8 Rev.B 01 / 2014

√ RoHS Compliant

Recommended Operating Conditions

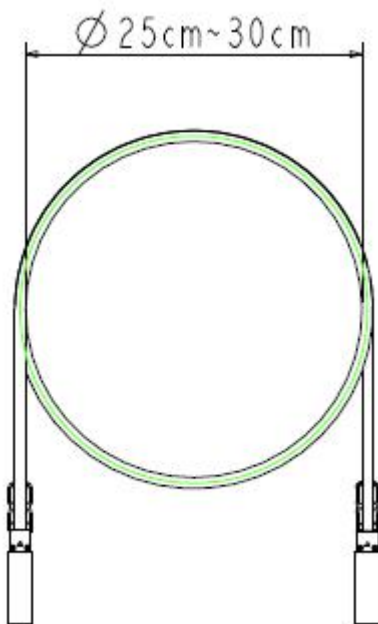
Parameter	Symbol	Min	Typical	Max	Unit
Storage Ambient Temperature		-40	+85		°C
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	VCC3	3.14	3.3	3.47	V
Power Dissipation	PD		2.5		W
Data Rate Per Lane		1	10		Gb/s

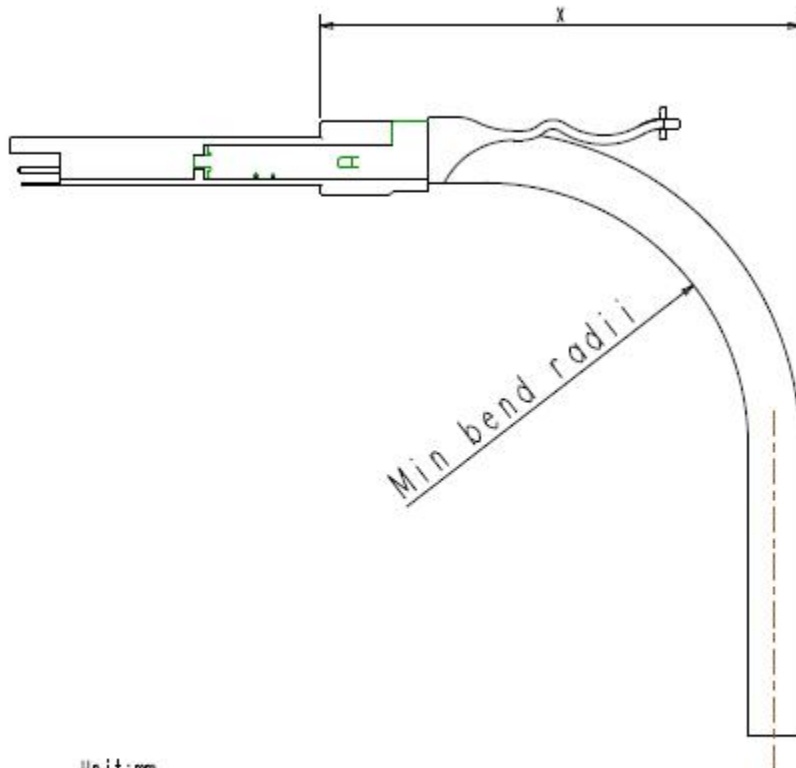
Pin Descriptions

Pin	Logic	Symbol	Name/Description	Notes
1	GND	Ground		1
2	CML-I	Tx2n		Transmitter Inverted Data Input
3	CML-I	Tx2p		Transmitter Non-Inverted Data Input
4	GND	Ground		1
5	CML-I	Tx4n		Transmitter Inverted Data Input
6	CML-I	Tx4p		Transmitter Non-Inverted Data Input
7	GND	Ground		1
8	LVTTL-I	ModSelL		Module Select
9	LVTTL-I	ResetL		Module Reset
10	Vcc Rx	+3.3V Power Supply Receiver		2
11	LVCMOSI/O	SCL		2-wire serial interface clock
12	LVCMOSI/O	SDA		2-wire serial interface data
13	GND	Ground		1
14	CML-O	Rx3p		Receiver Non-Inverted Data Output
15	CML-O	Rx3n		Receiver Inverted Data Output
16	GND	Ground		1
17	CML-O	Rx1p		Receiver Non-Inverted Data Output
18	CML-O	Rx1n		Receiver Inverted Data Output
19	GND	Ground		1
20	GND	Ground		1
21	CML-O	Rx2n		Receiver Inverted Data Output
22	CML-O	Rx2p		Receiver Non-Inverted Data Output
23	GND	Ground		1
24	CML-O	Rx4n		Receiver Inverted Data Output
25	CML-O	Rx4p		Receiver Non-Inverted Data Output
26	GND	Ground		1
27	LVTTL-O	ModPrsL		Module Present

28	LVTTL-O	IntL	Interrupt
29	Vcc Tx	+3.3V Power supply transmitter	2
30	Vcc1	+3.3V Power supply	2
31	LVTTL-I	LPMode	Low Power Mode
32	GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35	GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38	GND	Ground	1

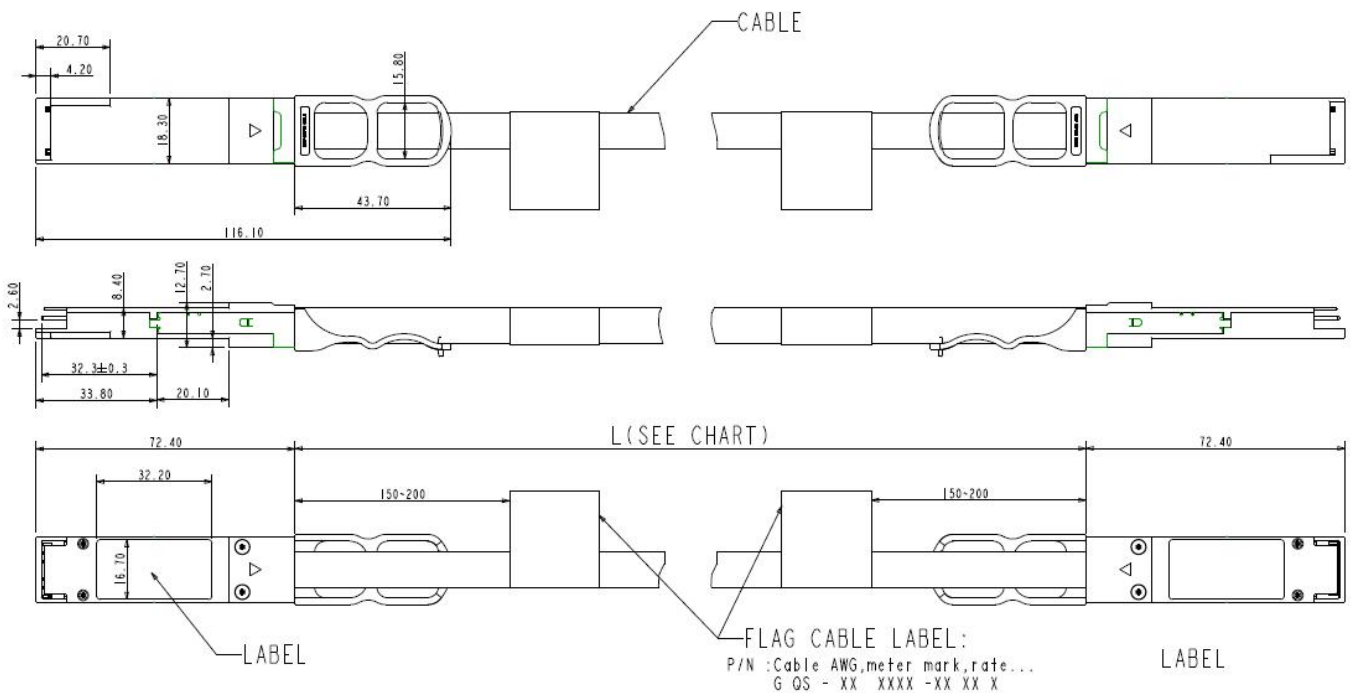
Mechanical Dimensions





Unit:mm

CABLE AWG	OD	min bend radii	*X*min.Distance lx bend
26	8.25	43.5	72.5



代码	长度范围 (m)	公差 (cm)
L	$L \leq 0.5$	± 3
	$0.5 < L \leq 5$	± 5
	$5 < L \leq 20$	± 8
	$20 < L$	± 10

QSFP+ Memory Map

